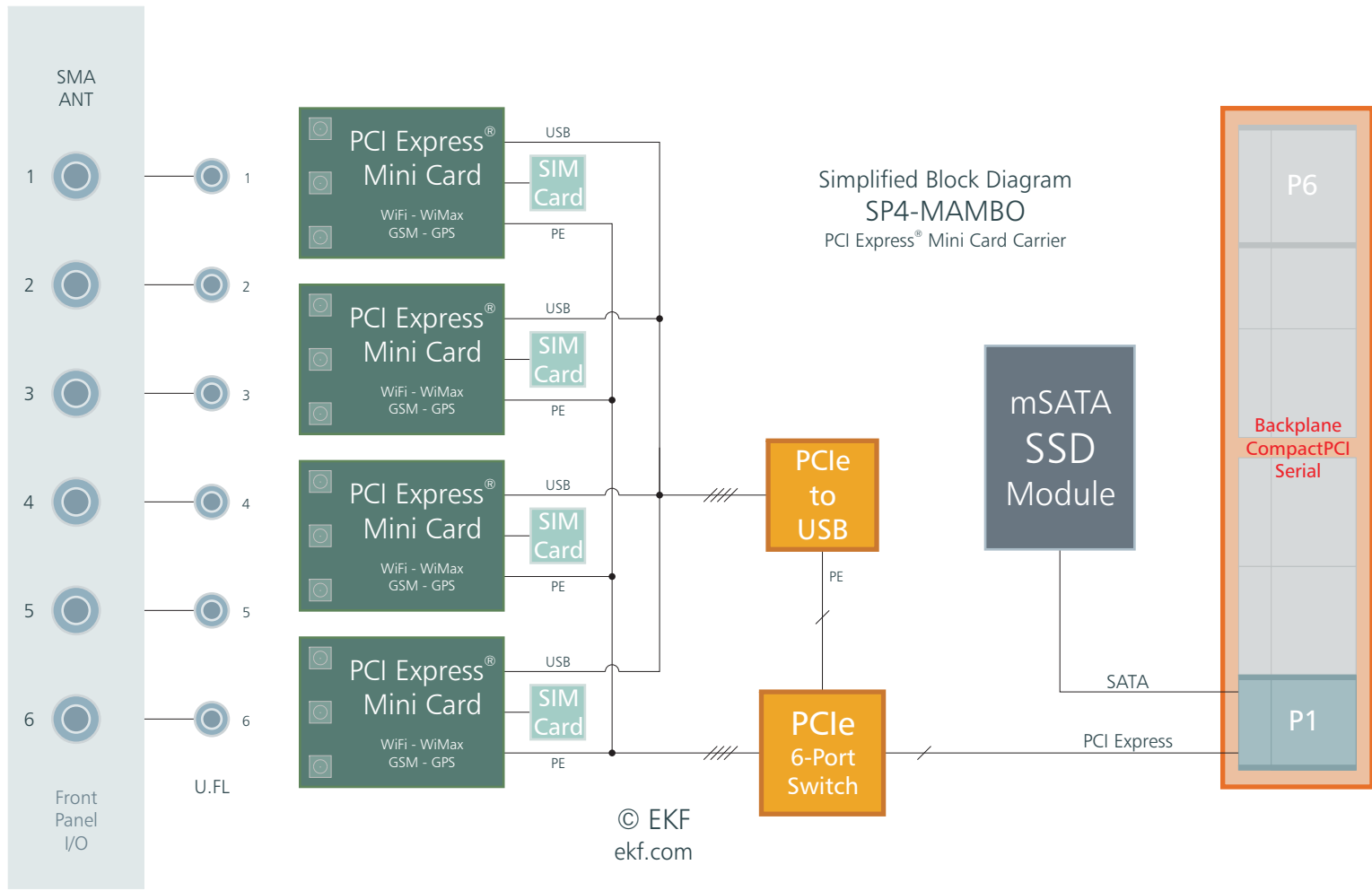


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SMA
ANT

1

2

3

4

5

6

Front
Panel
I/O

1

2

3

4

5

6

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